

3597 U.S. PTO  
10/016772



U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

|   |                           |              |                |                                       |                      |
|---|---------------------------|--------------|----------------|---------------------------------------|----------------------|
| APPL NUM<br>10016772  | FILING DATE<br>12/10/2001 | CLASS<br>326 | SUBCLASS<br>39 | GAU<br>2819                           | EXAMINER<br>D. Chang |
| <b>**APPLICANTS:</b> Bailis Robert; Kuhlmann Charles; Lingafelt Charles; Rincon Ann;  |                           |              |                |                                       |                      |
| <b>**CONTINUING DATA VERIFIED:</b>  |                           |              |                |                                       |                      |
| <b>** FOREIGN APPLICATIONS VERIFIED:</b>  |                           |              |                |                                       |                      |
| PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>  |                           |              |                |                                       |                      |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no<br>35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no<br>Verified and Acknowledged Examiners's initials |                           |              |                | ATTORNEY DOCKET NO<br>RPS920010131US1 |                      |
| TITLE : Method and system for use of an embedded field programmable gate array interconnect for flexible I/O connectivity   |                           |              |                |                                       |                      |

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|--|-----------|-----------------------------|--|
| <b>NOTICE OF ALLOWANCE MAILED</b>  |           | <b>CLAIMS ALLOWED</b>       |  |
| <b>ISSUE FEE</b>   |           | <b>Assistant Examiner</b>   |  |
| Amount Due   | Date Paid | <b>Primary Examiner</b>     |  |
| <input type="checkbox"/> <b>TERMINAL</b>   |           | <b>PREPARED FOR ISSUE</b>   |  |
| <b>DISCLAIMER</b>  |           | <b>Application Examiner</b> |  |
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